

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Howard E. Rhodes, et al.

Divisional of Application No.

10/295,952 filed November 18, 2002

Application No.: Not Yet Assigned

Group Art Unit: N/A

Filed: January 22, 2004

Examiner: Not Yet Assigned

For: TWIN PWELL WITH A RETROGRADE
PWELL FOR CMOS IMAGERS

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents
Washington, DC 20231

Dear Sir:

Pursuant to 37 C.F.R. §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the divisional patent application submitted herewith.

Those patent(s) or publication(s) which are marked with an asterisk (*) in the attached form PTO/SB/08 are not supplied because they were previously cited by or submitted to the Office in a prior application No. 10/295,952, filed November 18, 2002, and relied upon in this application for an earlier filing date under 35 U.S.C. §120.

While the information and references disclosed in this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. §1.56, it is not intended to constitute


an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Commissioner is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0107/P107-F. A duplicate copy of this paper is enclosed.

Dated: January 22, 2004

Respectfully submitted,

By 
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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	Not Yet Assigned
				Filing Date	January 22, 2004
				First Named Inventor	Howard E. Rhodes
				Group Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	3	Attorney Docket Number	M4065.0107/P107-F

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
*	A	4,374,700*		SCOTT et al.	02/1983	
*	B	5,150,177*		ROBINSON et al.	09/1992	
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*	Z		2-178864-A*		Yabu et al	07/1990		

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		Attorney Docket Number	M4065.0107/107-F
Sheet	2	of	3

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
*		Fossum, E., <u>Low Power Camera-on-a-Chip Using CMOS Active Pixel Sensor Technology</u> , 1995 IEEE, pps. 74-77. *	
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*		Mendis, S.K., et al., <u>Design of a Low-Light-Level Image Sensor with On-Chip Sigma-Delta Analog-to-Digital Conversion</u> , Proc. SPIE Vol. 1900, July 1993, pps. 31-39. *	
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*		Nakamura, J., et al., <u>CMOS Active Pixel Image Sensor with Simple Floating Gate Pixels</u> , IEEE Transactions on Electron Devices, Vol. 42, No. 9, September 1995, pps. 1693-1694. *	
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Sheet	3	of	3	Attorney Docket Number	M4065.0107/107-F

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